Analog Circuit Design: Mixed A/D Circuit Design, Sensor Interface Circuits and Communication Circuits

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Some examples have been presented of BiCMOS circuit design in mixed- signal ASICs. It is by no means an exhaustive list. Many cells have not been mentioned, including 300MHz CMOS Video DACs, 20 Bit Sigma-Delta ADCs, Real-time and Switched-Cap Filters, Line Drivers and Receivers and many more. References. [1] J.Corcoran "High Speed Sample and Hold and Analog-to-Digital Converter Circuits",Advances in Analog Circuit Design, April1992. [2] Y.S.Yee,L.M.Terman and L.G.Heller "A lmV MOS comparator," IEEE J. of Solid-State Circuits, vol. SC-13, June 1978. [3] M.Timko and P.Holloway "Circuit Techniques for Achieving High Speed- High Resolution AID Conversion" IEEE Journal of Solid-State Circuits, vol. SC-15, No.6, December 1980. [4] W.H. Gross "New High Speed Amplifier Design, Design Techniques and Layout Problems", Advances in Analog Circuit Design, April 1992. MIXED SIGNAL ASIC DESIGN FOR AUTOMOTIVE AND INDUSTRIAL APPLICATIONS H. Casicr Mietcc Alcatel, Brussels, Belgium ABSTRACT This paper describes the special aspects of mixed signal design in the ASIC environment. The knowledge of the application specific environment and of the function of the ASIC can be used advantageously to lower the cost and to enhance the performance at all levels of design. Several examples of this cost improvement and performance enhancement at device, circuit and system level are shown.

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